

RESUME

Rupali V.Chothe
C/o Mr.Vilas Chothe
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Date of birth: 22nd January, 1985

Objective:

To have a challenging position in teaching field where the combination of my knowledge along with my creative thinking to contribute in the students development and my growth.

Academic Profile:

1. M.E. (VLSI and Embedded) from K. K. W. I. E. E. R. ,Nasik, Pune University with First class.
2. B.E. (Electronics & Communication) from A.I.T.S., Saurashtra University, Rajkot in May 2006 with 72.83% (First Class with Distinction)
3. H.S.C. from G. T. Sheth Vidyalaya in March 2002 with 73.00% (First Class-Distinction)
4. S.S.C. from Swaminarayana Gurukul in March 2000 with 86.50% (Distinction)

Experience:

1. As an Asst. Prof. in K.K.W.Engg. college,Nasik.(From 11th August 2007 till date)
2. As a lecturer in V.V.P.Engg.college, Rajkot (in the academic year 2006-07).

M.E. Project:

“E-Nose as a Vehicle Exhaust Analyzer Using Supervised Learning of ANN”.

B.E. Project:

Designing a Data Acquisition System using ELT-II kit

(Based on VHDL-Quatrus-II software)

(Assigned from the VLSI company SLS (System Level Solutions, Anand))

Subjects taught:

Electronic devices and circuits, Electronic design practice, Signal coding and Estimation Theory, Solid State devices and circuits, Industrial Electronics, Basic Electronics Engineering.