

Name: Abhishek Srivastava

Position: Assistant Professor

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Date of Birth: 24th October 1991



Educational Qualifications: PhD Power System Engineering (Thesis submitted)
MTech. Power System Engineering
B. Tech. Electrical and Electronics Engineering

Work Experience (Yrs): 3 Yrs (Research)

Area of Specialization: Power Systems Engineering

Number of Papers Published (In Journals): 07

Number of Papers Published (In Conference): 13

Number of Patents Published: 1

Number of Patents Granted: NIL

Number of Books Published: NIL

Number of Training Programs/Seminars/Workshops: 15

Memberships: NIL

Place: Nashik

Date: 07-04-2022

Abhishek Srivastava