

BIO-DATA

Email: rmchandramore@kkwagh.edu.in

Name: Rasika Madhukar Chandramore

Designation: Assistant Professor in Robotics & Automation Dept.

Contact No.: 8850673056

Office Address : Department of Robotics & Automation, K. K. Wagh Institute of Engineering Education and research, Nasik.

Area of Research: VLSI & EMBEDDED DESIGN, ROBOT OPERATING SYSTEM, SENSOR TECHNOLOGY.



EDUCATIONAL QUALIFICATIONS:

Sr. No.	Degree	Specialization	Board/University	Marks	Division	Year
1	ME	E&TC	K K WAGH COE,NASHIK. (Pune university)	CGPA 8.550	First class with Distincti on	2017
2	BE	E&TC	N.D.M.V.P COE,NASHIK. (Pune university)	63. 13	First class	2015

PROJECT DETAILS:

Modelling ,Designing And Analysis Of Phase Locked Loop Using Pyxis tool of Mentor Graphics

The Phase Locked Loop system has been around since 1932. The versatility of the PLL systems and where it can apply makes it very useful. It can be applied to automobiles as well as cellular chips. This seminar work presents the design and analysis of a Phase Locked Loop for various technologies. The All blocks of the Phase Locked Loop were designed independently and then integrated together. The Phase Frequency Detector, Charge Pump, Low Pass Filter, VCO, Frequency Divider and Level Shifter topologies and circuits are described in detail along with some major design tradeoffs and critical issues. The uses and implementation of the kinds of frequency divider are demonstrated. Their design tradeoffs are utilized to make the design cost effective and robust.

ACTIVITY:

1. Attended e-PGpex-2016, conducted by Savitribai Phule Pune University at MIT, Pune on 18th June 2016.
2. Exhibited project work titled, “Study And Implementation Of Phase Locked Loop” at e-PG Project Exhibition(e-PGpex-2017 organized by Savitribai Phule Pune University at AISSMS, on 8 June 2017.

PAPER PUBLISHED/PRESENTED :

1. 3rd International Conference on Emerging Trends in Engineering and Management Research (ICETEMR-17) held at (IETE) Institution of Electronics and Telecommunication Engineers, Pune India on 30th July 2017
2. International Journal of advance research in science and engineering vol.no.6 Issue no.7 ,july 2017.

CERTIFICATIONS:

1. Certification of training in '**BSNL SILVER CERTIFIED ENGINEER COURSE**' under BSNL-AICTE EETP.
2. Certification of training in '**BSNL GOLD CERTIFIED ENGINEER COURSE**' under BSNL-AICTE EETP.
3. Certification of training in '**BSNL PLATINUM CERTIFIED ENGINEER COURSE**' under BSNL-AICTE EETP.
4. Certification of Workshop organized on "**Industrial Approach for Electronics**" by Prof chaudhari from Electronics study centre.
5. Certification of **Arya Technology** in Embedded and C designing.

HOBBIES: Reading & Music