BIODATA

1. Name: Prof. Shraddha Vitthal Shelke

2. Date Of Birth: 31ST December1987

3. Educational Qualification: M. E.(VLSI and Embedded System Design)
   B.E. (Electronics)

4. Work experience:
   a. Teaching: 7 years
   b. Industry :   7months

5. Subjects taught :
   - System on Chip
   - Employability Skills and Mini Project
   - Digital electronics
   - Electromagnetics
   - Basic Electronics Engineering
   - VLSI and Embedded System Design

6. No. of papers published in
   - National Conferences: **03**
   - International Conferences: **01**

7. Details Projects carried out:
   - Title of Project: "Handwritten character recognition using multiscale neural network training technique"

8. **Workshop attended**
   1. Two days workshop on Flipped Learning on 13th -14th June 2018.
   2. Two-Week ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design 30th Jan 2017 to 4th Feb 2017
   3. Attended Workshop on "Design & Analysis of Modern Antennae" on 9th and 10th Dec 2016
   4. Attended Workshop on skill development on 28th -29th Oct 2015
   5. Attended Seminar on "MATLAB & Simulink in engineering Education" at Nashik on 14 th Jan 2014
   6. Attended faculty orientation workshop for revised syllabus

   for the subject Electromagnetics in 2014

   for the subject Mini project & Seminar in 2014

   for the subject Computer organization in 2013

   for the subject Digital Electronics in 2013
   7. Attended 10 days ISTE workshop under National Mission on Education through ICT(MHRD)for the subject Analog Electronics

8. Attended one day seminar on VLSI Design organized by Cadence Design System, Pune at Sinhgad Institute of Engineering, Pune June 2013
9. Attended "ePGCON" Conference organized by Cummins C.O.E .Pune on 23rd April 2012
10. Two days Management Development program on 16th and 17th Dec 2011
11. Workshop on " Embedded technology, Embedded Linux,Effective Learninig Techniques for Embedded Engineering,Finite State Machine" on 5th March 2011
12. Workshop on Syllabus exploration for T.E.(E & TC/Electronics)(2008 Course) on 7th Feb 2011

9. Workshops organized in department

1. Organized Arduino workshop by Mr. Ambekar for TE E&TC in year 2014-15
2. Contributed as a Teaching assistant in two week ISTE workshop under National Mission on Education through ICT(MHRD) for the subject Basic Electronics

10. Institute level work, committee members institute or outside. University work
1. Departmental CAP coordinator
2. Departmental Website coordinator
3. Timetable Co-coordinator
4. Class coordinator of SE E&TC since 2014-15 to 2016-17, FE E&TC 2013-14
5. First year Basic electronics engineering subject coordinator 2013-14

11. Industrial visit
1. Visit at National Association of Blind , Nashik on 10th Aug .2018 SE E&TC A.Y.2018-19 sem 1
2. Industrial Visit at Shivananda Electronics, Nashik for TE E&TC students A.Y.2017-18 sem 2
3. Industrial visit at Caprihans Pvt.Ltd Nashik for TE E&TC students A.Y.2017-18 sem 2
4. Arranged Industrial visit for SE E&TC students at Vaitarna Hydro power station in A.Y. 2014-15 SEM 1
5. Arranged Industrial visit for SE E&TC students at MOTWANE Pvt. Ltd, Nashik in A.Y. 2014-15 SEM 2

12. Workshops organized in department

1. Web Page and mobile application development workshop by Mr. Sachin Gavande, Mr Ajay Avhad on 17,18 Feb 2018
2. Organized Arduino workshop by Mr. Ambekar for TE E&TC in year 2014-15
3. Contributed as a Teaching assistant in two week ISTE workshop under National Mission on Education through ICT(MHRD) for the subject Basic Electronics

SHELKE SHRADDHA VITTHAL